

<p style="text-align: right;">O I P E AN 26 2004 PATENT &amp; TRADEMARK OFFICE</p> <p><b>LIST OF REFERENCES CITED BY APPLICANT</b> (Use several sheets if necessary)</p>					ATTY. DOCKET NO.:	APPLICATION SERIAL NO.:	
					85170-4800	10/679,978	
					APPLICANT:		
					Stephen Maxwell Parkes		
<b>FILING DATE:</b> October 6, 2003					GROUP:	2819	
<b>U.S. PATENT DOCUMENTS</b>							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
DC	AA	4,569,041	2/1986	Takeuchi et al.	370	60	
DC	AB	6,072,944	6/2000	Robinson	395	500.05	
	AC						
	AD						
<b>FOREIGN PATENT DOCUMENTS</b>							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
DC	AE	EP 0 338 558	10/1989	Europe	—	—	X
DC	AF	EP 0 867 820	1/1998	Europe	—	—	X
DC	AG	EP 0 969 631	1/2000	Europe	—	—	X
DC	AH	EP 1 096 736	5/2001	Europe	—	—	X
DC	AI	WO 98/02822	1/1998	PCT	—	—	X
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
DC	AK	Parkes, "Space Wire: Links, Nodes, Routers and Networks" ECSS-E-50-12 Space Engineering pp. 1-101, 6/2000					
DC	AL	Swamy et al. "OO-VHDL, Extensions to VHDL" IEEE (1995) No month					
—	AM	Guenier et al. "A Generic Architecture for On-Chip Packet-Switched Interconnections"					
DC	AN	Benini et al. "Networks on Chips: A new SoC Paradigm" SOC Designs IEEE (2002) No month					
DC	AO	A. Tsutsui et al., "Special Purpose FPGA for High-speed Digital Telecommunication Systems", NTT Optical Network Systems Laboratories , pp 486-491, IEEE (1995) No month					
DC	AP	D'Angelo et al., "Modular Design of Communication Node Prototypes" pp. 170-175 IEEE (1997) No month					
DC	AQ	Kaptanoglu et al.,(XP-000868474) "A new high density and very low cost reprogrammable FPGA architecture"pp 3-12, (1999) No month					
DC	AR	Vranesic "The FPGA Challenge", Dept. of Electrical and Computer Engineering, pp 121-126 IEEE (1998). No month					
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<small>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							

LIST OF REFERENCES CITED BY APPLICANT <i>(Use several sheets if necessary)</i>				ATTY. DOCKET NO.:	APPLICATION SERIAL NO.:		
				85170-4800	10679978		
				APPLICANT: Stephen Maxwell Parkes	FILING DATE: GROUP:		
U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILED DATE IF APPROPRIATE
<i>DC</i>	AA	4,903,260	2/1990	Boettle et al.	370	60	
<i>↑</i>	AB	5,359,536	10/1994	Agrawal et al.	364	489	
	AC	5,457,410	10/1995	Ting	326	41	
	AD	5,483,178	1/1996	Costello et al.	326	41	
	AE	5,682,107	10/1997	Tavana et al.	326	41	
	AF	5,880,597	3/1999	Lee	326	41	
	AG	5,880,598	3/1999	Duong	326	41	
	AH	5,815,726	9/1998	Cliff	395	800.01	
<i>↓</i>	AI	6,084,429	7/2000	Trimberger	326	41	
<i>DC</i>	AJ	6,191,612 B1	2/2001	Agrawal et al.	326	39	
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					—	—	YES <input checked="" type="checkbox"/> NO <input type="checkbox"/>
<i>DC</i>	AK	0 360 540	3/1990	Europe	—	—	X
	AL						
OTHER REFERENCES ( <i>Including Author, Title, Date, Pertinent Pages, Etc.</i> )							
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<b>EXAMINER</b> <i>Darl D. Chay</i>				<b>DATE CONSIDERED</b> <i>9/23/0X</i>			
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